

Title: LEAD FRAME FOR SEMICONDUCTOR PACKAGE INCLUDING PLURAL LEAD FRAMES HAVING THIN PARTS OR HOLLOWS ADJACENT THE TERMINAL ROOTS

Inventor(s): Chikao IKENAGA et al.

Serial No.: 09/850 213

Docket No.: OPS Case 533

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FIG. 1 (PRIOR ART)

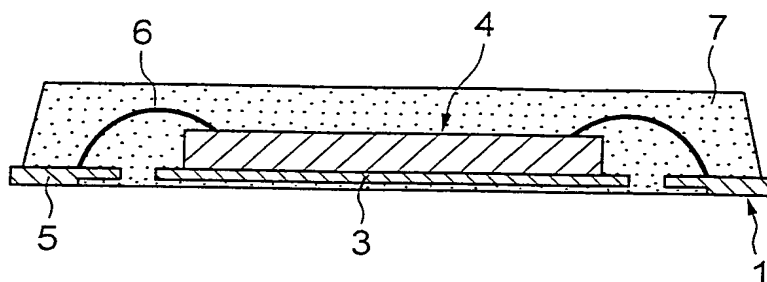
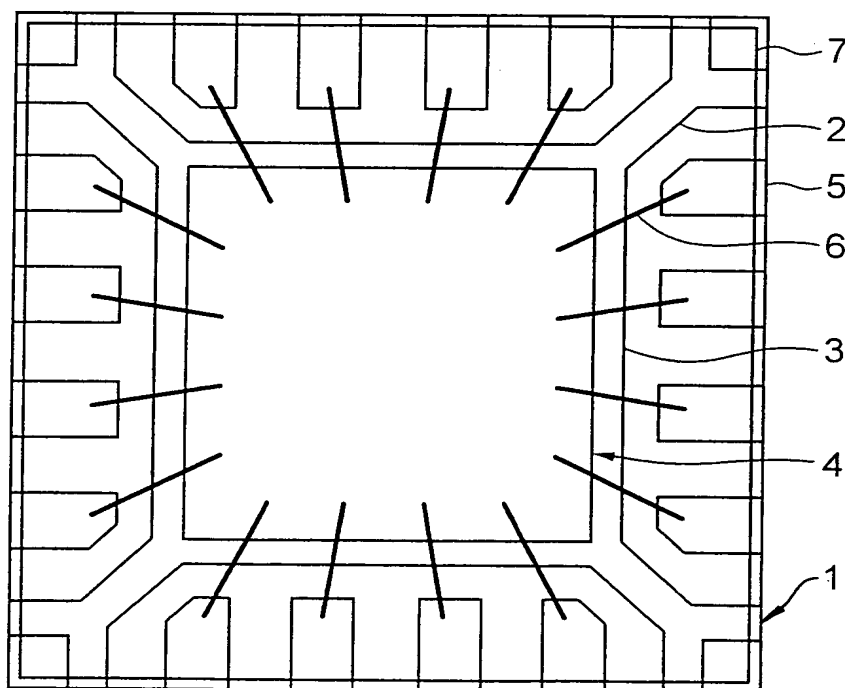


FIG. 2 (PRIOR ART)





FRAME FOR SEMICONDUCTOR PACKAGE INCLUDING PLURAL LEAD FRAMES HAVING THIN PARTS OR HOLLOWES ADJACENT THE TERMINAL ROOTS

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FIG. 3(A) (PRIOR ART)

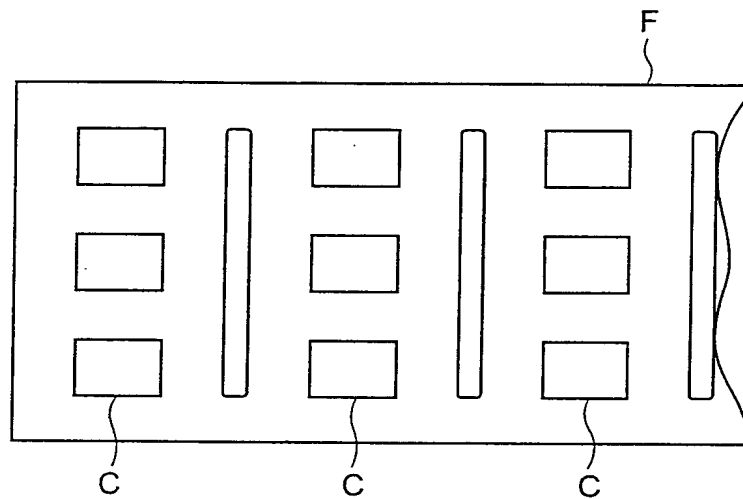


FIG. 3(B) (PRIOR ART)

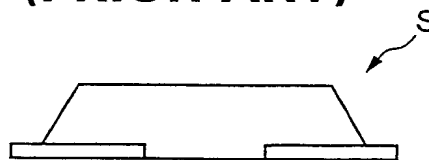




FIG. 4(A) (PRIOR ART)

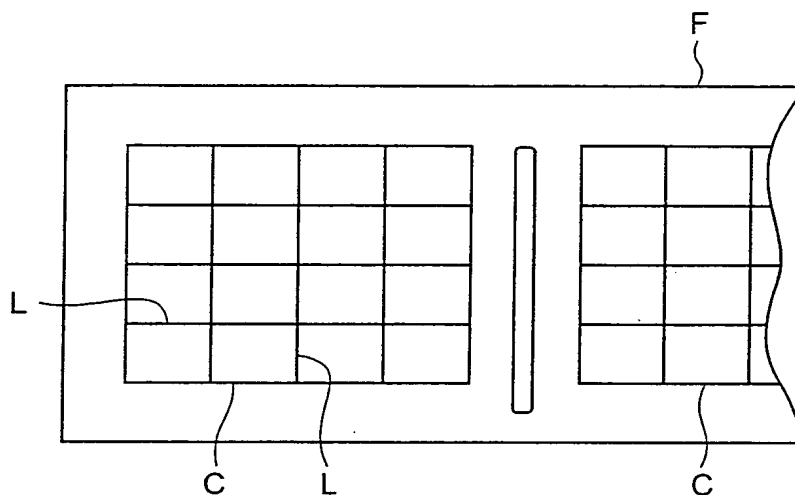
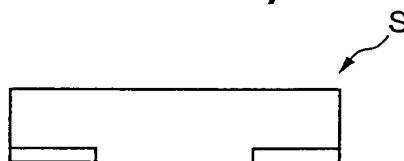


FIG. 4(B) (PRIOR ART)





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FIG. 5 (PRIOR ART)

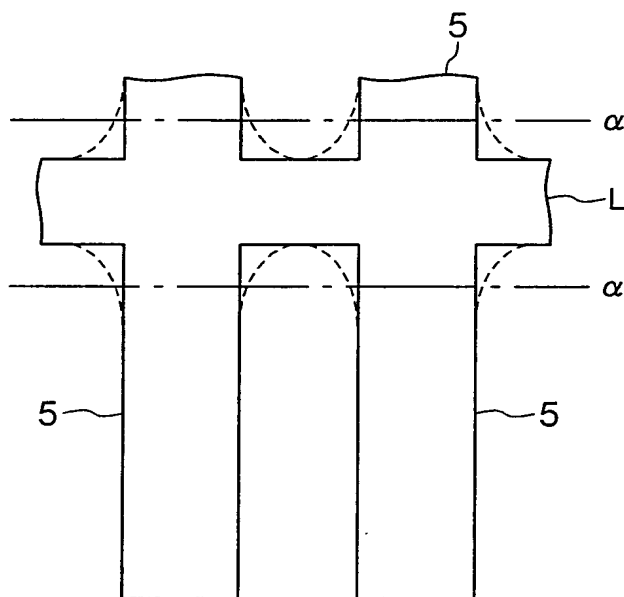
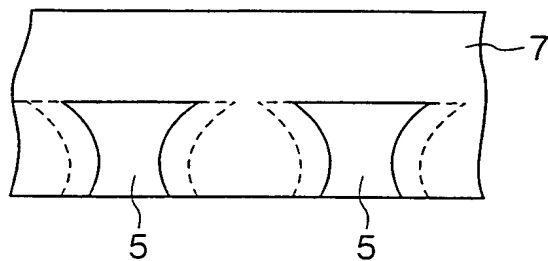


FIG. 6 (PRIOR ART)





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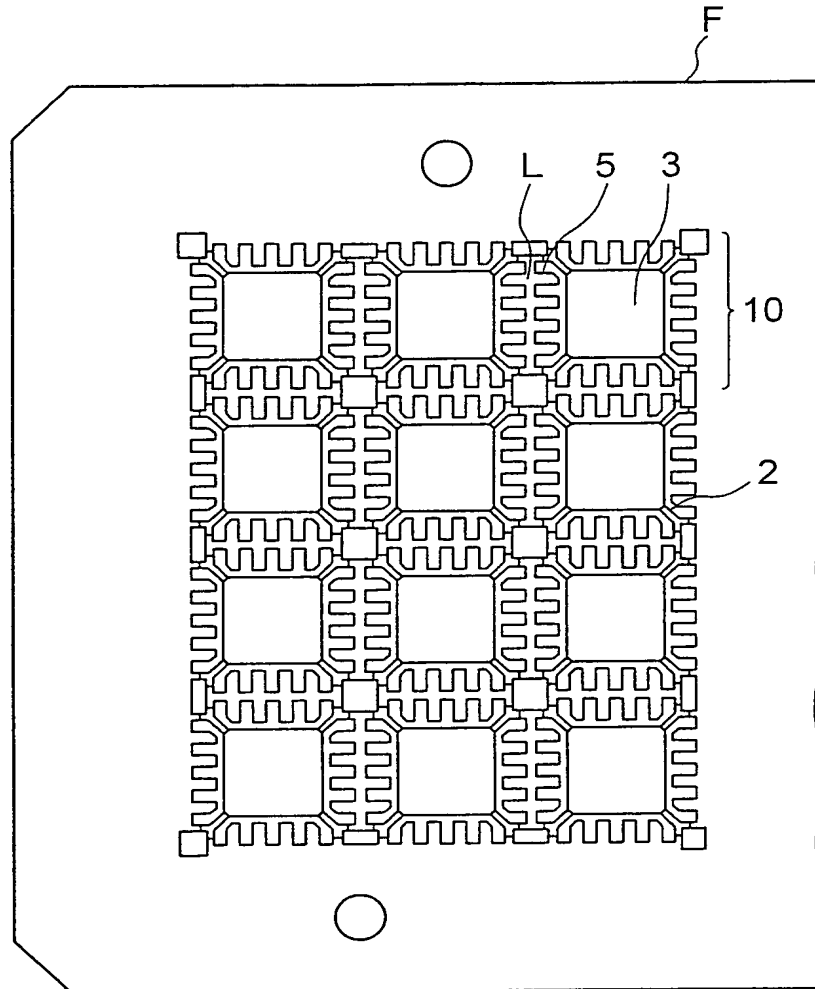
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FIG. 7





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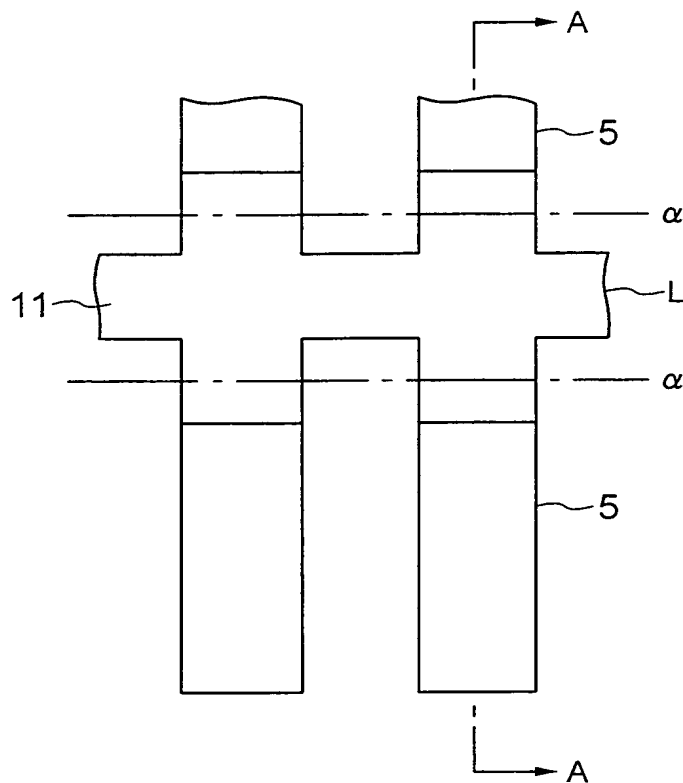


FIG. 8

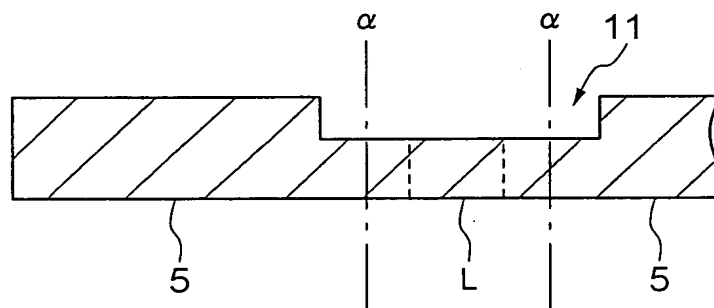


FIG. 9



Title:

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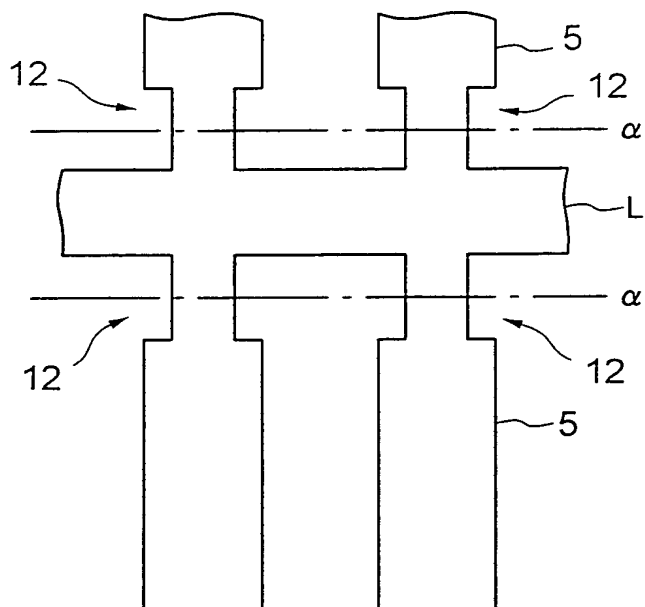


FIG. 10

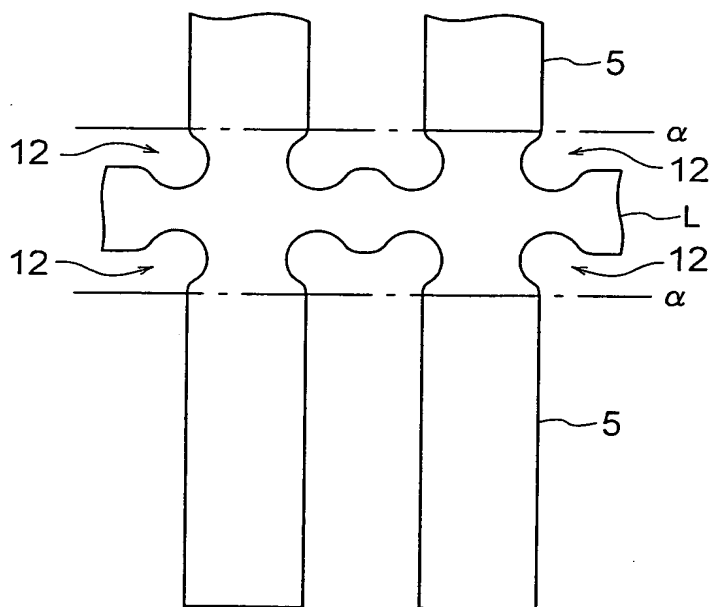


FIG. 11